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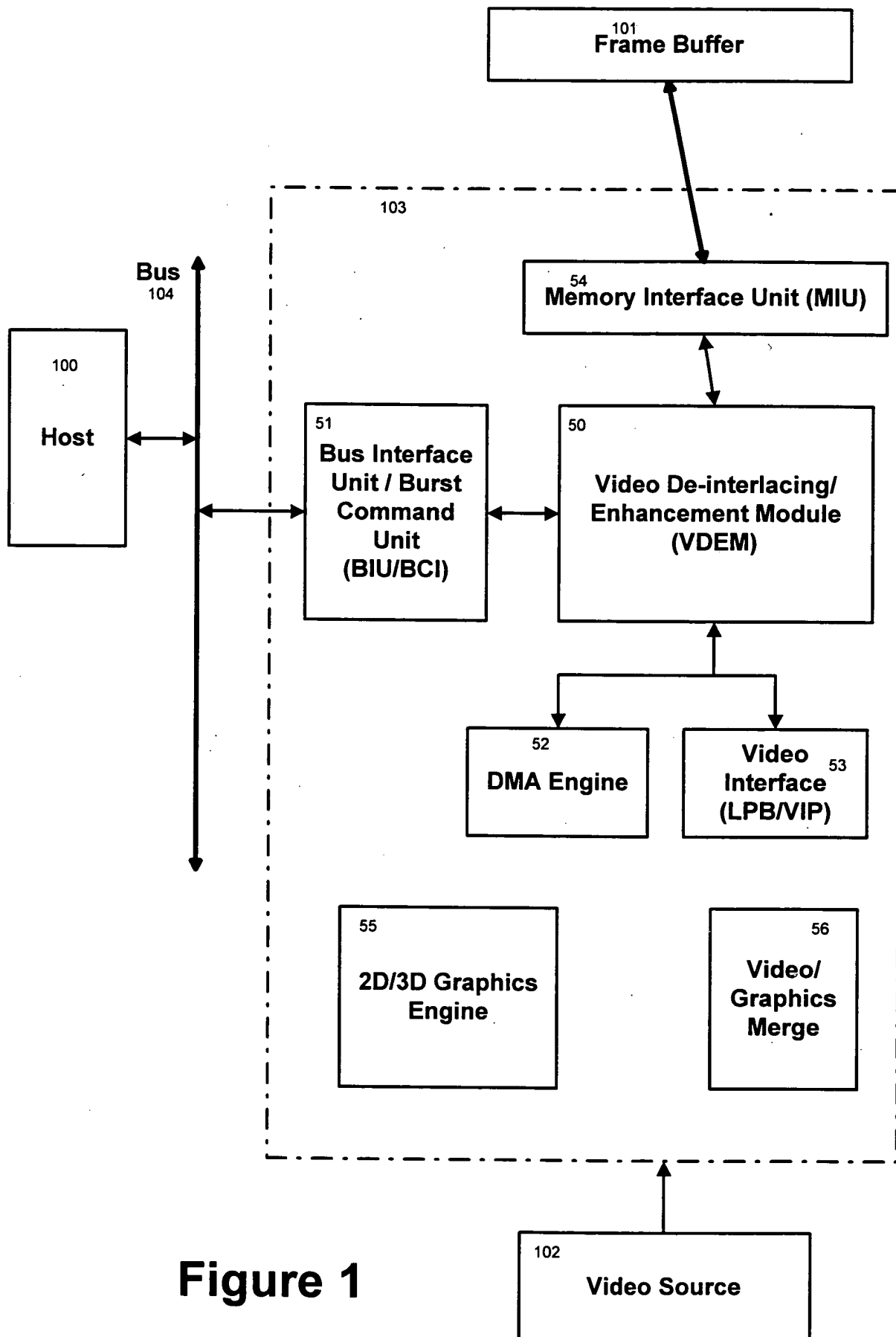


Figure 1

87

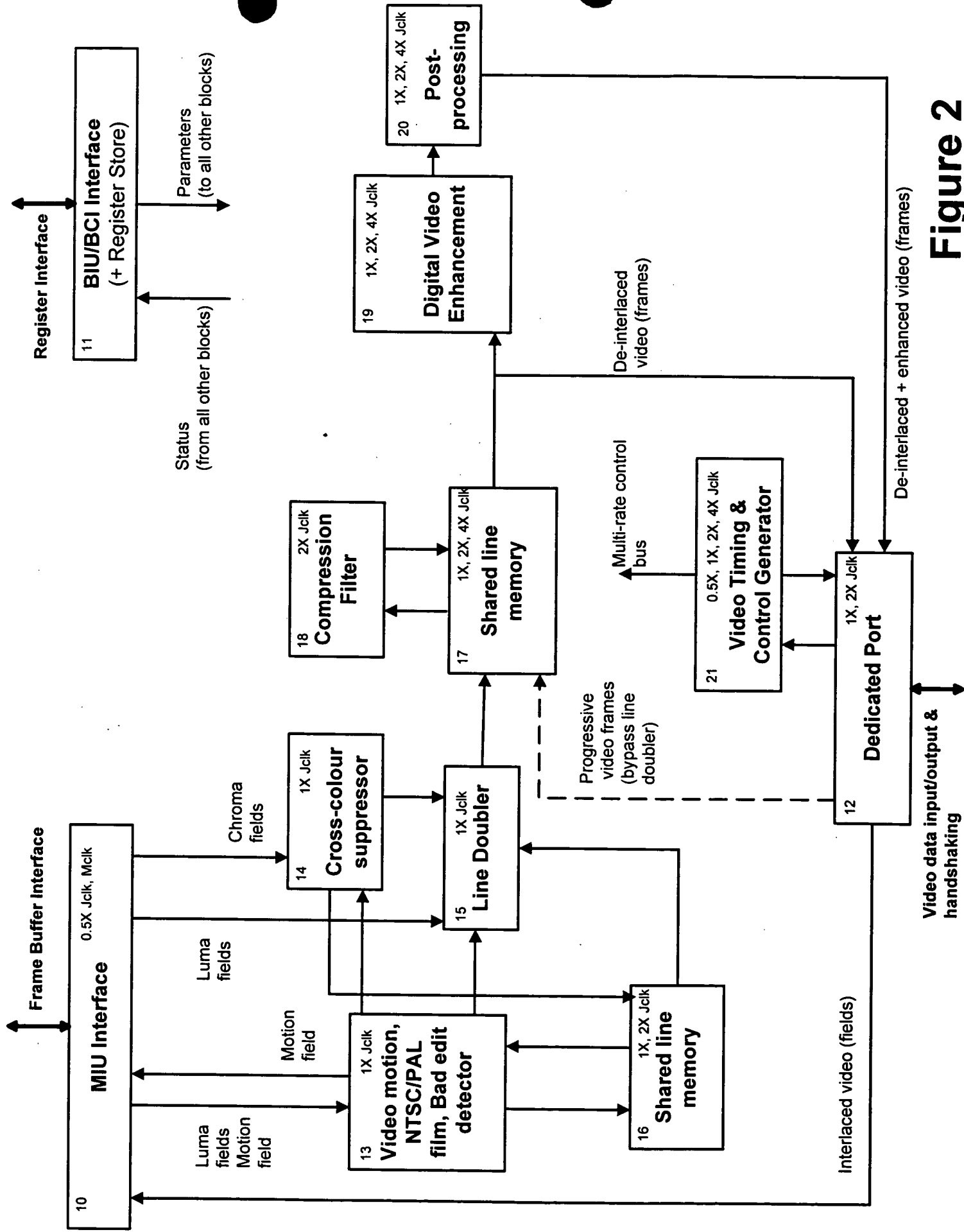


Figure 2

Figure 1 is a timing diagram for a video signal. It shows two horizontal bars representing the timing of data input and output. The top bar is labeled "Timing Buffer (no input or output data valid)" and shows a "Timing Buffer" region (302) and a "Timing Buffer" region (308). The bottom bar is labeled "Timing Buffer (no input or output data valid)" and shows a "Timing Buffer" region (302) and a "Timing Buffer" region (308). The diagram also shows a "Register update point" (304) and a "Register update point" (308). The diagram is divided into two main sections: "Odd lines (original)" and "Even lines (interpolated)".

### Figure 3

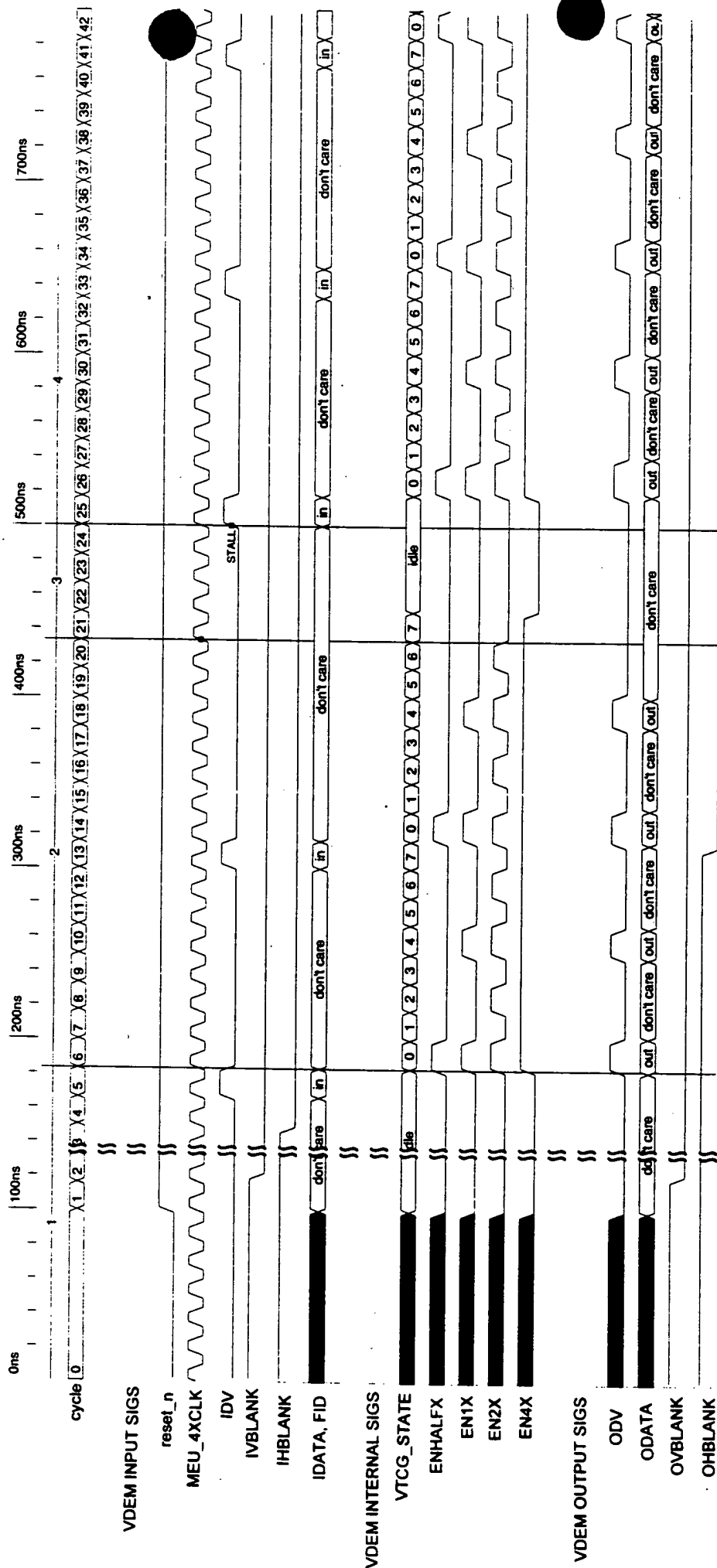


Figure 4

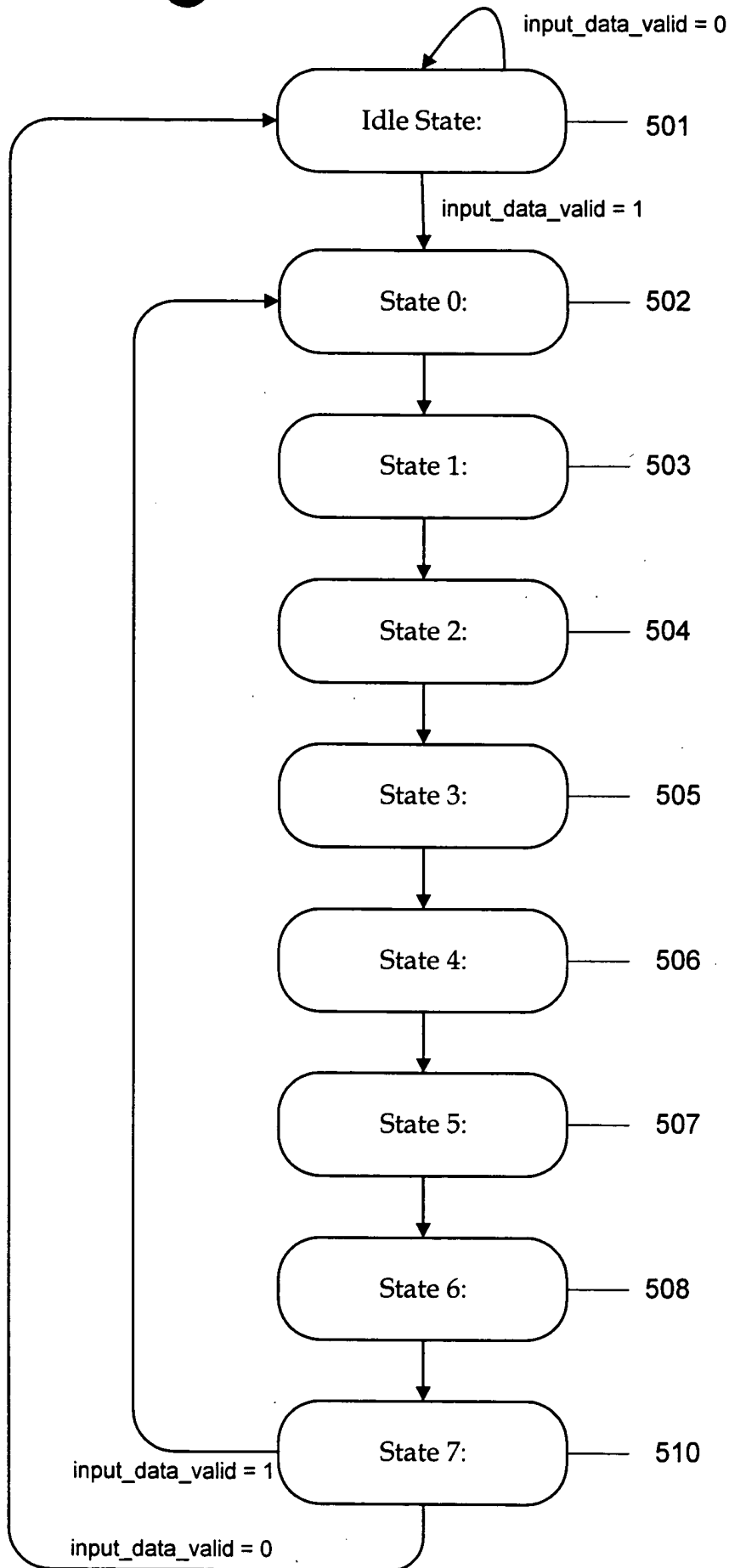


Figure 5

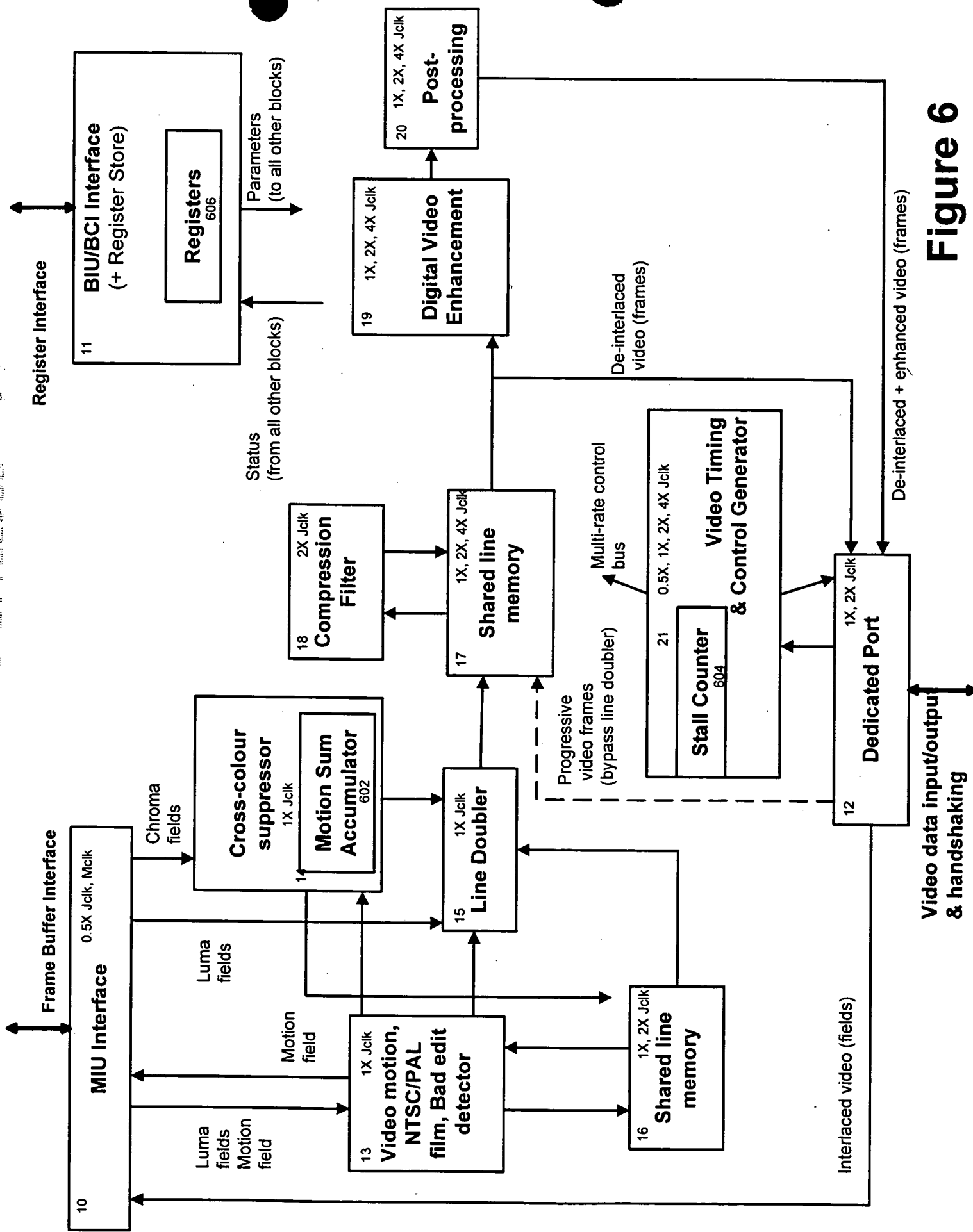


Figure 6

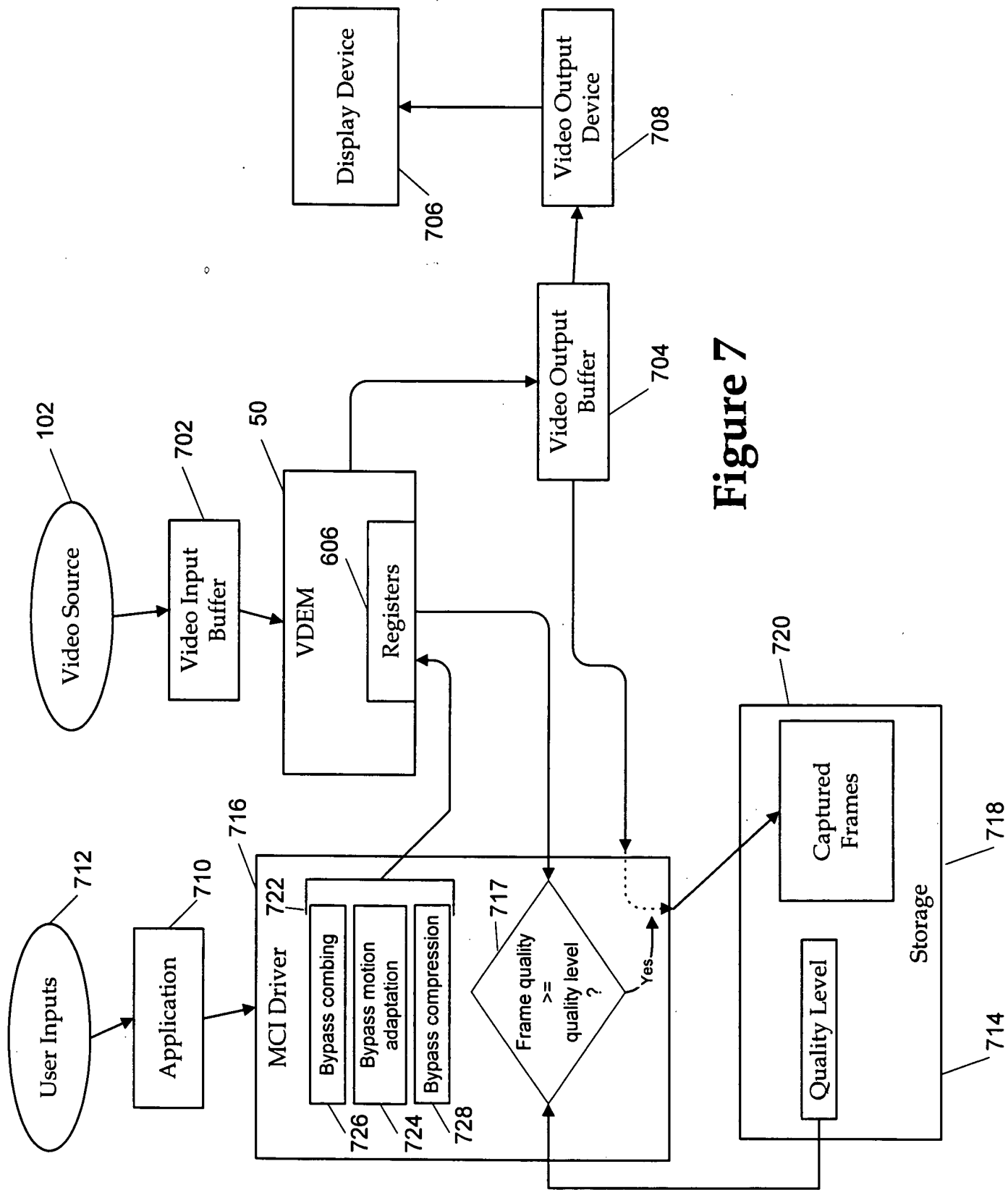


Figure 7